DOCKET NO. TI-34945

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In repatent application of

Applicant:

Josey G. Angilivelil

Serial No.: Filed:

Unknown Herewith

For:

SYSTEM AND METHOD TO FACILITATE EVALUATION OF INTEGRATED

CIRCUITS THROUGH DELAY TESTING

Art Unit: Examiner:

Unknown Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

1. Pursuant to 37 C.F.R. 1.97 and 1.98, and in compliance with 37 C.F.R. 1.56, the Office's attention is directed to the patents, publications and other information listed on the attached PTO-1449. A copy of each listed foreign patent document and other publication is enclosed. No U.S. Patents nor any published U.S. Patent Applications are enclosed.

Regarding any document, publication or other information for which a date is not given on the attached PTO-1449, Applicant(s) believe(s) the same may qualify as "prior" art to this application and should be treated accordingly, although Applicant(s) reserve(s) the right to contest the prior art status of any document, publication or information, should issue arise.

- 2. Regarding each listed document that is not in the English language, an English-language translation accompanies this Statement as indicated on the attached PTO-1449 or a concise explanation of the relevance of the document is set forth in the following document(s):
 - (a) Copy of each English language version of a search report indicating the degree of relevance found by the foreign office of each document being submitted from the search report.
 - (b) ____ Attachment entitled "Concise Explanation of Relevance of Non-English Language Documents".
 - 3. Pursuant to 37 C.F.R. 1.97(b) this Statement is being filed (one must be checked):
 - (a) X Within 3 months of the filing date or date of entry into the National Stage.
 - (b) _____ Before the mailing date of a first Office Action on the merits. If this Statement is not filed before the mailing date of a first Office Action on the merits, the required certification is given below or, in the absence thereof, the Office is authorized to charge the required fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668 for consideration of this Statement.

	(c)	After the period set forth in 37 C.F.R. 1.97(b) but before the mailing date of either a final action or a notice of allowance.
		(1) The required certification is given below, or
		(2) Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(p) for consideration of this Statement or
		(3) Charge the fee set forth in 37 C.F.R. 1.17(p) to Deposit Account No. 20-0668.
	(d)	After the mailing date of either a final action or a notice of allowance, but before payment of the issue fee. Petition hereby is made for consideration of this Statement and the required certification is indicated below.
	·	(1) Enclosed is a check covering the fee set forth in 37 C.F.R. 1.17(i)(1), or
		(2) Charge the fee set forth in 37 C.F.R. 1.17(i)(1) to Deposit Account No. 20-0668.
4.	Certificat	ion (if applicable)
	(a)	The undersigned hereby certifies that each item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than 3 months prior to the filing of this Statement.
	(b)	The undersigned hereby certifies that no item of information contained in this Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the undersigned's knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. 1.56(c) more than 3 months prior to the filing of this Statement.
5.	The Com: 20-0668.	missioner is hereby authorized to charge any additional fees or credit any overpayment to Deposit Account No.

Respectfully Submitted,

TAROLLI, SUNDHEIM, COVELL, & TUMMINO L.L.P.

Dy. -1-

Reg. No. 39,334

CUSTOMER NUMBER: 23494

TAROLLI, SUNDHEIM, COVELL & TUMMINO LLP 526 Superior Avenue, Suite 1111 Cleveland, Ohio 44114-1400 (216) 621.2234

Approved for use through 04/30/2003. OMB 0651-0031

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			U. S. PATENT	DOCUMENTS			
Examiner Initials*	No.1		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		Number-Kind Code ^{2 (I known)}	 	 	Figures Appear		
		^{US-} 5,606,567	02/25/1997	Agrawal, et al.			
	_	^{US-} 6,131,181	10/10/2000	Bushnell, et al.			
		^{US-} 6,205,559 B1	03/20/2001	Sakaguchi			
		^{US-} 6,574,760 B1	06/03/2003	Mydill			
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		FORE	IGN PATENT DOCU	MENTS			
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comment on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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Attorney Docket Number

TI-34945

Examiner Name

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of

Sheet 2

Examiner

NON PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²				
		SAMY R. MAKAR and EDWARD J. MCCLUSKEY, "Iddq Test Pattern Generation for Scan Chain Latches and Flip-Flops", Center for Reliable Computing Standford University, (date unknown)					
		SAGAR SBADE and DUNCAN M. WALKER, "Iddq Test: Will It Survive the DSM Challenge", Department of Computer Science Texas A&M Unviersity, pp. 1-18, (date Unknown)					
		KRISTIAN WIKLUND, TOMAS MAGNUSSON and PETER DAHLGREN, "Switch-Level Fault Simulation and Test Generation For Competing Bridging Faults", Department of Computer Engineering, Chalmers University of Technology, (date unknown)					
		PETER C. MAXWELL and ROBERT C. AITKEN, "Test Sets and Reject Rates: All Fault Coverages ARe Not Created Equal", IEEE Design & Test of Computers, pp. 42-51, March 1993					
		SHARAD C. SETH and VISHWANI D. AGRAWAL, "Characterizing the LSI Yield Equation from Wafer Test Data", IEEE Transactions on Computer-Aided Design, Vol. CAD-3, No. 2, pp. 123-126, April 1984					

Date

Signature Considered EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not

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¹ Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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